Silicon P Channel MOS FET High Speed Power Switching

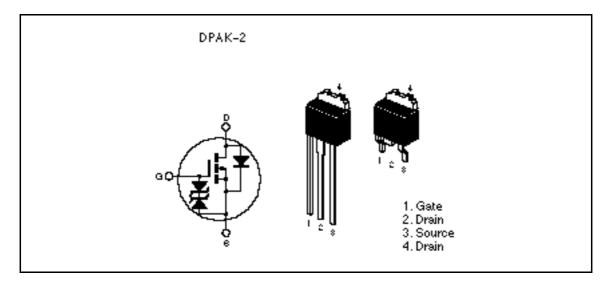
# HITACHI

ADE-208-655B (Z) 3rd. Edition Jun 1998

#### Features

- Low on-resistance  $R_{DS(on)} = 0.08$  typ.
- 4V gate drive devices.
- High speed switching.

#### Outline





#### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit			
Drain to source voltage	V <sub>DSS</sub>	-60	V			
Gate to source voltage	V <sub>GSS</sub>	±20	V			
Drain current	I <sub>D</sub>	-15	A			
Drain peak current	Note1 D(pulse)	-60	A			
Body-drain diode reverse drain current	I <sub>DR</sub>	-15	A			
Avalanche current	AP Note3	-15	А			
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	19	mJ			
Channel dissipation	Pch Note2	30	W			
Channel temperature	Tch	150	°C			
Storage temperature	Tstg	–55 to +150	°C			

Note: 1. PW 10µs, duty cycle 1 %

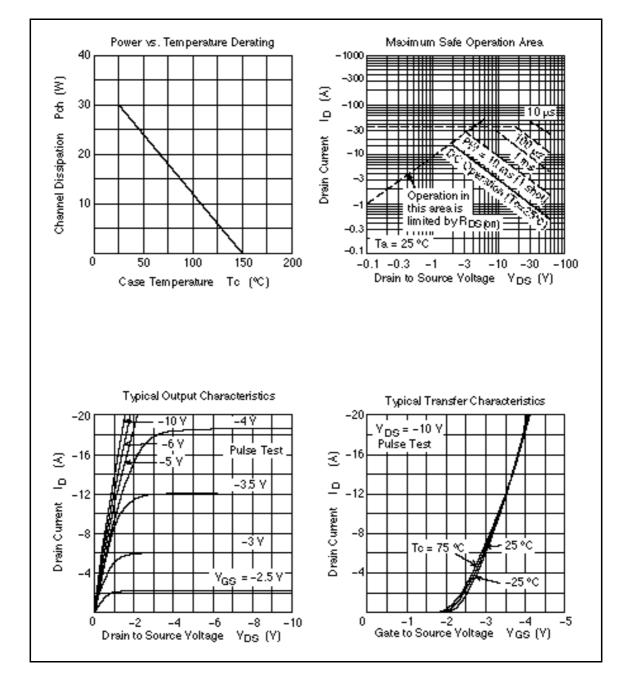
2. Value at Tc = 25°C

3. Value at Tch =  $25^{\circ}$ C, Rg 50

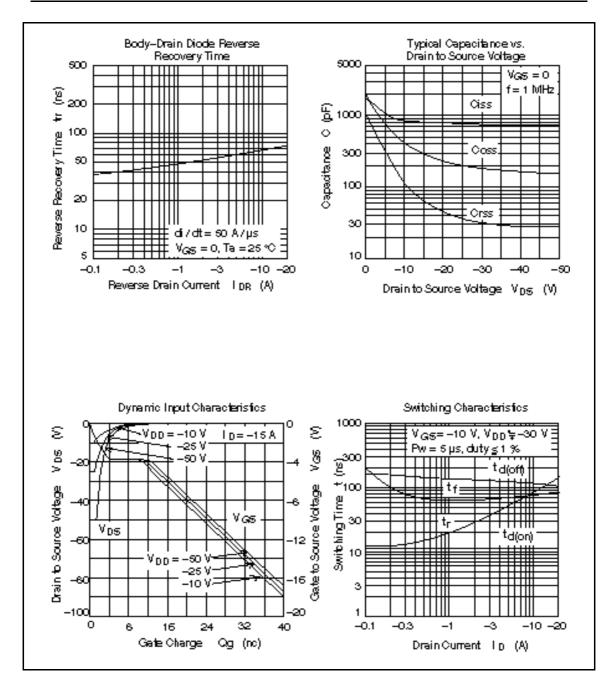
#### **Electrical Characteristics** (Ta = 25°C)

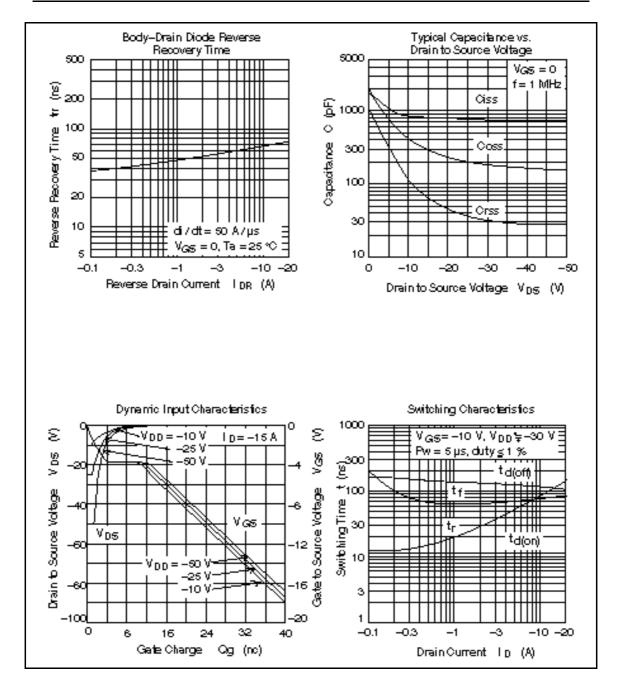
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_{\rm D} = -10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_{g} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	—	—	-10	μA	$V_{\rm DS} = -60 \ V, \ V_{\rm GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	-1.0	_	-2.0	V	$I_{\rm D} = -1$ mA, $V_{\rm DS} = -10$ V
Static drain to source on state	$R_{DS(on)}$	—	0.08	0.10		$I_{\rm D} = -8A, V_{\rm GS} = -10V^{\rm Note4}$
resistance	$R_{\text{DS(on)}}$		0.11	0.16		$I_{\rm D} = -8A, V_{\rm GS} = -4V^{\rm Note4}$
Forward transfer admittance	y <sub>fs</sub>	6.5	11	_	S	$I_{\rm D} = -8A, V_{\rm DS} = -10V^{\rm Note4}$
Input capacitance	Ciss	—	850	_	pF	$V_{DS} = -10V$
Output capacitance	Coss		420	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		110	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	12	_	ns	$V_{GS} = -10V, I_{D} = -8A$
Rise time	t,		75	_	ns	R <sub>L</sub> = 3.75
Turn-off delay time	$t_{d(off)}$	_	125	_	ns	_
Fall time	t <sub>f</sub>		75	—	ns	-
Body-drain diode forward voltage	$V_{DF}$	—	-1.1	—	V	$I_{\rm F} = -15$ A, $V_{\rm GS} = 0$
Body–drain diode reverse recovery time	t <sub>rr</sub>	—	70	_	ns	$I_{F} = -15A, V_{GS} = 0$ diF/ dt =50A/µs

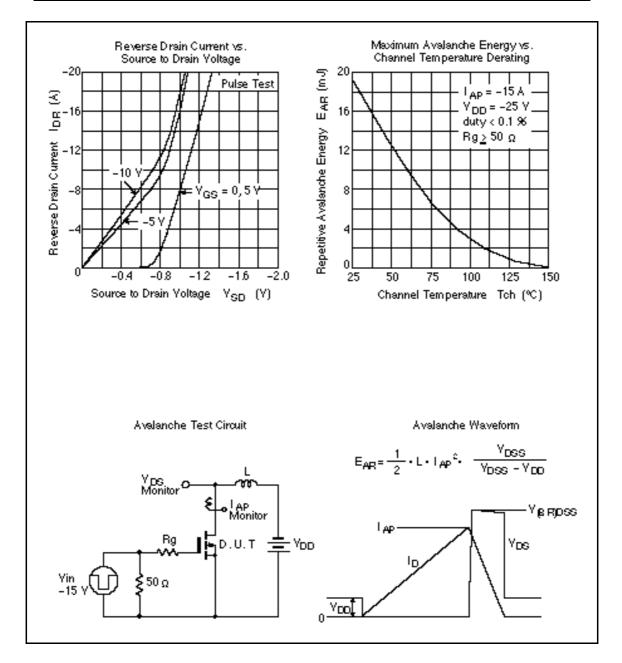
Note: 4. Pulse test

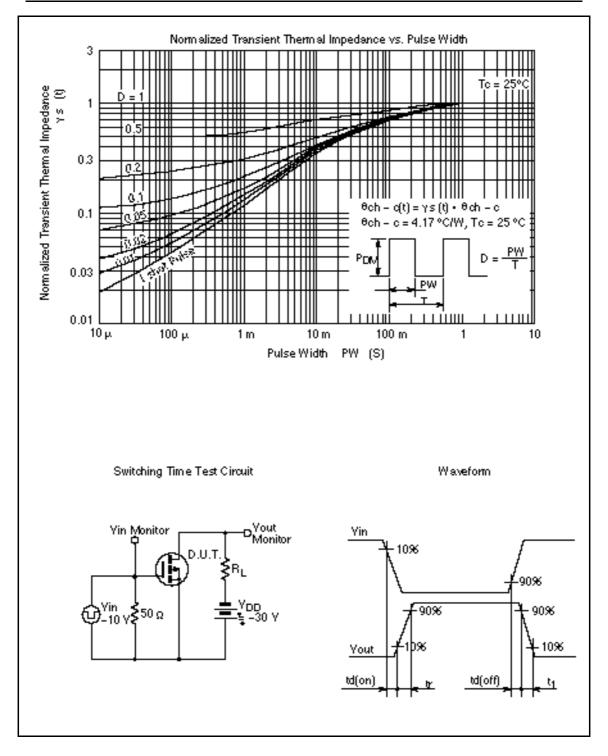


#### **Main Characteristics**

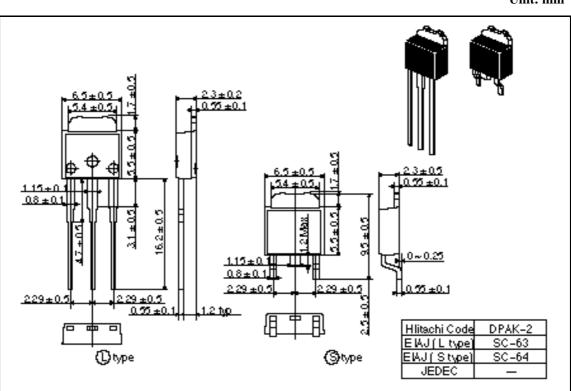








### **Package Dimensions**



Unit: mm

#### Cautions

- Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent, copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
- 2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
- 3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
- 4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as fail-safes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
- 5. This product is not designed to be radiation resistant.
- 6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
- 7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

#### Δ Hitachi, Ltd.

Gemiconductor & IC Di u Nippon Bidg., 2-6-2, Ohle-macht Chinoda-ku, Tokho 100-0004, Japan Tel : Tokuo (08) 8070-0111 Fall: (08) 8270-5101 For further information write to: Hitschi Europe GmbH - Hitschi Europe Ltd. Hilachi Gemiconductor (America) inc. Continental Europe 2000 Slema Roint Parkmay/ Domacher Straße 3

Brikbane, C.A. 94005-1897 D-85622 Feldurchen UEA München Tel: 800-285-1601 Tel:089-9.91 80-0 Fas:808-097-0447 Fas:089-929 80-00

Bectronic Components Div Northern Europe Headquarters Veltebrook Park Lower Cookham Road Maldenhead Berkshire BL68YA United Kingdom Tel: 01628-585000 Fam: 01628-585160

Hitschi Asia Pie, L14. 16 Colliver Quanalco-oo Uni1706, North Tomer, Hitschi Tomer Bingapore 049818 Tel: 585-2100 Fas: 585-1588

Hilachi Asia (Hong Kong) Lid. World Rhance Centre Harbour City Canton Road Teim Cha Teul Komioon Hong Kong Tel: 07859018 Fall: 27806071

Copyright OH Bachi, Ltd., 1998. All rights reserved. Rinted in Japan.